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Date: October 11, 2010 Name: Jasper W. Dockrey, Reg. No. 33,868 Signature: /Jasper W. Dockrey/

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UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
JOLY et al.)	
)	Examiner: Zarneke, David A.
Serial No.: 10/561,299)	
)	Group Art Unit: 2891
Filing Date: December 15, 2005)	
)	Confirmation No.: 3881
For: INTEGRATED CIRCUIT ON)	
HIGH-PERFORMANCE CHIP)	
)	
Docket No: 9905/34 (BIF023273USA))	

RESPONSE UNDER 37 C.F.R. §1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

In response to the office action of May 11, 2010, in the above-reference application, the applicants respectfully request reconsideration of the final rejection in view of the amendment and remarks presented herein. The applicants assert that entry of the amendment will place their application in condition for allowance.

Amendments to the claims are set forth in the listing of claims beginning at page 2 of this paper.

The applicants' remarks begin at page 7 of this paper.

BRINKS HOFER GILSON & LIONE
NBC Tower – Suite 3600, 455 N. Cityfront Plaza Drive, Chicago, IL 60611-5599

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